

Team

2187-71

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Jeffrey R. Wilcox Et Al.

Method And Apparatus For Reducing Power

Consumption In A Memory Bus Interface By

Selectively Disabling Sense Amplifiers

Group Art Unit:

2187

Serial No.:

10/038,960

3 5 **F**/

Examiner:

Glenn Gossage

Filed:

For:

January 2, 2002

Atty. Dkt. No.:

ITL.0668US

(P12986)

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

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Technology Center 2100

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated June 17, 2004 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.0668US).

July 2, 2004

Date

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Respectfully submitted,

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Date of Deposit: July 2, 2004

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450,

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Monica Jacobs

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